

IN THE SPECIFICATION

Please amend the specification as follows:

1. Amend paragraph [0008] as follows:

Heretofore, amplifier circuits as described above are described for example in patent document 1 and patent document 2. An example of a detailed configuration of an amplifier circuit for implementing the LINC system is shown in FIG. 3. At amplifier circuit 10a shown in FIG. 3, baseband signals Sai, Saq, Sbi, Sbq, which constitute constant-envelope signals Sa, Sb after orthogonal demodulation, are generated from baseband input signals Si, Sq by digital signal processing at constant-envelope signal IQ generating section 15. Then, after each baseband signal is converted to an analog signal using D/A converters 16a, 16b, 16c, 16d, in orthogonal modulator 17, ~~and the converted signals are orthogonally modulated by orthogonal modulator 17~~ having two orthogonal modulators 19a, 19b, 19c, 19d using local signal L₁, which is generated by local oscillator 20 and phase-shifted by phase shifters 18a, 18b. The modulated signals are then added at connections 19e, 19f to obtain two constant-envelope signals Saif, Sbif. Then, at mixer 21a, 21b, frequency conversion is carried out by mixing each signal with a local signal L₂ supplied by local oscillator 22 to obtain signals Sarf, Sbrf converted to a carrier frequency. Final amplification is then carried out at amplifiers 12, 13, and combining is carried out at combining section 14 to obtain an output signal as a result.

2. Amend paragraph [0043] as follows:

Orthogonal modulator 113 orthogonally modulates baseband signals Sai, Saq, Sbi, Sbq which are converted to analog signals, using local signal LO' which is generated by local oscillator 116 and phase-shifted by phase-shifters 115a, 115b. Orthogonal modulator 113 then adds orthogonally-modulated baseband signals Sai, Saq, Sbi, Sbq at connections 117a, 117b to generate generates first constant-envelope signal Saif and second constant-envelope signal Sbif, ~~and~~ Orthogonal modulator 113 outputs these signals to phase-shifters 102a, 102b, respectively. Local oscillator 116 in orthogonal modulator 113 is an oscillation circuit such as a frequency synthesizer or the like employing a voltage controlled oscillator (VCO) controlled by a phase-locked loop (PLL). Further, phase-shifters 115a, 115b in orthogonal modulator 113 are, for example, hybrid phase-shifters using microstrip lines.

3. Amend paragraph [0143] as follows:

In addition to mixers 114a to 114d, ~~and~~ phase-shifters 115a, 115b and connections 117a, 117b described in the first embodiment, local oscillator 1011 is also provided in orthogonal modulator 1010. Orthogonal modulator 1010 orthogonally modulates baseband signals Sai, Saq, Sbi, Sbq to generate first constant-envelope signal Sarf and second constant-envelope signal Sbif respectively described in the first embodiment.